Molded, SOT-23 Resistor/Capacitor Network



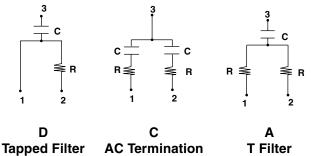
Vishay's R/C Network, packaged in the standard SOT-23, can be strategically placed on your PC board to do localized filtering. The R/C Network can be located at the point of emission before transients are carried through the design.

The sophisticated process of integrating the Resistor and Capacitor on a single substrate provides you with higher performance and more consistent results over discrete components. A real estate savings will also be gained.

Applications include EMI/RFI suppression and AC termination. These networks, in the SOT-23, along with Vishay's high component count R Networks and R/C Networks in a variety of standard IC packages, provides you with the exact solution for your redesign or new design.

Visit our website for the total picture on available R Networks and R/C Networks from our guaranteed stock program.

SCHEMATIC



FEATURES

• Lead (Pb)-free standard



COMPLIANT

- Resistor and capacitor **integrated** into a Thin Film Network
- · Filters at the source of emissions
- More consistent performance characteristics than discretes

TYPICAL PERFORMANCE

	TCR	TOLERANCE
RESISTOR	200	10 %
	тсс	TOLERANCE
CAPACITOR	200	20 %

VR TOOLED VALUES ⁽¹⁾			
SCHEMATIC	R (Ω)	C (pF)	
D	33	47	
С	47	47	
А	100	80	

Note

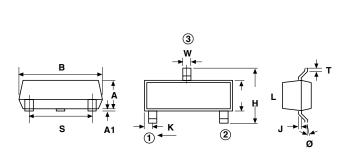
⁽¹⁾ Consult Application Engineering for Custom Values

STANDARD ELECTRICAL SPECIFICATIONS			
TEST		SPECIFICATIONS	CONDITIONS
Resistance Rang	e	10 Ω to 500 Ω	
TCR:	Absolute	± 200 ppm/°C	0 °C to + 70 °C
Tolerance:	Absolute	± 10 % Standard (R)	
	Absolute	± 20 % Standard (C)	at 1 MHz and V _{RMS} over + 10 °C to + 70 °C
Power Rating:	Package	1 W at + 70 °C	
Power Rating/Res	sistor	100 mW	
Capacitance Range (pF)		10 - 80	
Breakdown Voltage		25 - 45 V	
Operating Temperature Range		0 to + 70 °C	
Storage Temperature Range		- 55 °C to + 125 °C	

Vishay Thin Film



DIMENSIONS AND IMPRINTING in inches and millimeters

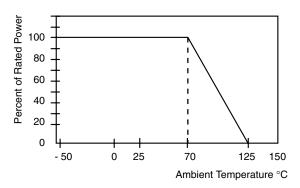


JEDEC STANDARD TO-236				
DIMENSION	INCHES		MILLIMETERS	
DIMENSION	MIN.	MAX.	MIN.	MAX.
А	0.027	0.040	0.70	1.02
A1	0.001	0.004	0.02	0.15
В	0.105	0.120	2.67	3.04
S	0.071	0.079	1.80	2.00
W	0.015	0.021	0.38	0.54
L	0.083	1.03	2.10	2.64
Н	0.047	0.055	1.20	1.40
Т	0.050	0.157	0.13	0.40
J	0.003	0.008	0.089	0.15
к	0.017	0.022	0.44	0.55
Ø	0	8°	0	8°

IMPRINTING		
	SCHEMATIC	
VRA	AA	
VRC	AC	
VRD	AD	

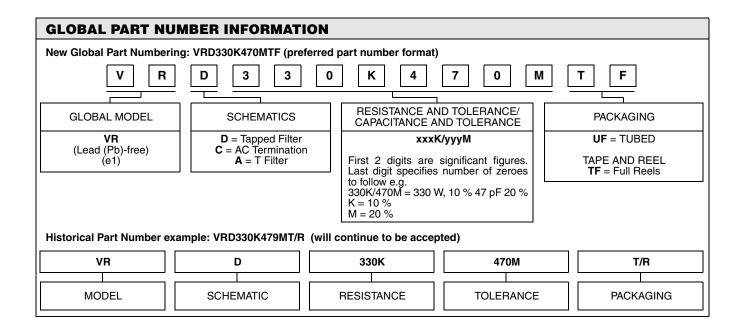
MECHANICAL SPECIFICATIONS		
Resistive Element	Tantalum Nitride	
Capacitive Element	Thin Film	
Substrate Material	Silicon	
Body	Molded Epoxy	
Terminals	Copper Alloy	
Plating	100 % Sn Matte	
Lead Coplanarity	0.0005 Inches	
Marking Resistance to Solvents	Permanency testing per MIL-STD-202, Method 215	

DERATING CURVE



PACKAGING INFORMATION			
MODEL	LEADS	TAPE AND REEL	
VR	3	3000	







Vishay

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